IN THE CLAIMS:

Please cancel claims 13 and 20 without prejudice to or disclaimer of the subject matter recited therein.

Please amend claim 12 as follows:

LISTING OF CURRENT CLAIMS

Claims 1-11. (Canceled)

Claim 12. (Currently Amended) A solid state disk module comprising:

a) an IDE interface ~~for~~ located on a circuit board and connecting the disk module to a main board of a ~~computer;~~ computer, the IDE interface having an extending interface;

b) a flash memory controller electrically connected to the IDE interface, and controlling data access and specifying an address for data storage;

c) a flash memory array having a plurality of flash memories, the flash memory array being connected to the flash memory controller for saving data, the flash memory controller and the flash memory array are electrically connected to ~~a~~ the circuit board, the flash memory controller and the flash memory array are enclosed by a casing; and

d) a power source having a power input terminal and a power output terminal and connected to the flash memory controller and the flash memory array to supply a working ~~voltage.~~ voltage, wherein the power source is integrally formed with the IDE interface as a single connector.

Claim 13. (Canceled)

Claim 14. (Previously Presented) The solid state disk module according to claim 12, wherein the flash memory controller is a single chip controller.

Claim 15. (Previously Presented) The solid state disk module according to claim 12, wherein the flash memory controller is an MX9691 controller.

Claim 16. (Previously Presented) The solid state disk module according to claim 12, wherein the plurality of flash memories comprises ten flash memories divided into five groups.

Claims 17-18. (Canceled)

Claim 19. (Previously Presented) The solid state disk module according to claim 12, wherein the IDE interface is electrically connected to the circuit board.

Claim 20. (Canceled)